A FPGA-based Soft Multiprocessor System for JPEG Compression

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Abstract

Fig.1. Microblaze processor core block diagram Similar to most of RISC processors, Microblaze

Hardware System

MicroBlaze 0		MicroBlaze 1
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consumption.

In software, DPRAM driver is replaced by FIFO driver, which provides the same communication and synchronization mechanism. The partitioning and architecture of the application code is intact.

4.4 DMA Interconnection

DMA controller has its advantage in c

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We thank Prof. Jef van Meerbergen for his guidance and reviewing of this paper. We thank Joris van Emden and Marcel Lauwerijssen for providing high quality JPEG encoder reference code and support afterwards. Prof. Henk Corporaal providesdædit qrdsq nd F

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